2SJ278

Silicon P-Channel MOS FET

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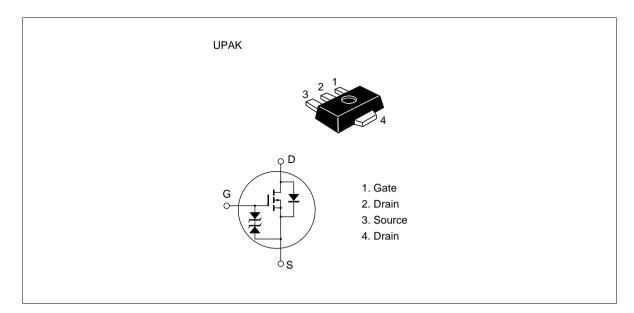
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for switching regulator, DC-DC converter

Outline





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Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{ t DSS}$	-60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I _D	-1	A
Drain peak current	l _{D(pulse)} *1	-4	A
Body to drain diode reverse drain current	I _{DR}	-1	A
Channel dissipation	Pch*2	1	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

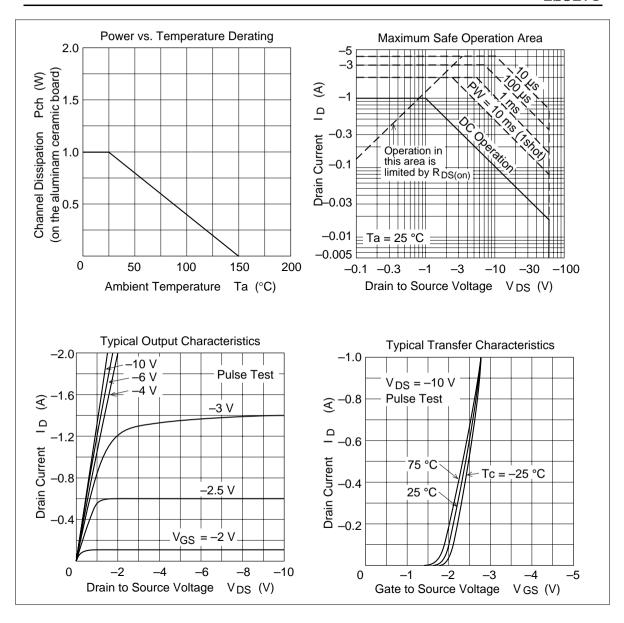
2. Value on the alumina ceramic board (12.5×20×0.7 mm)

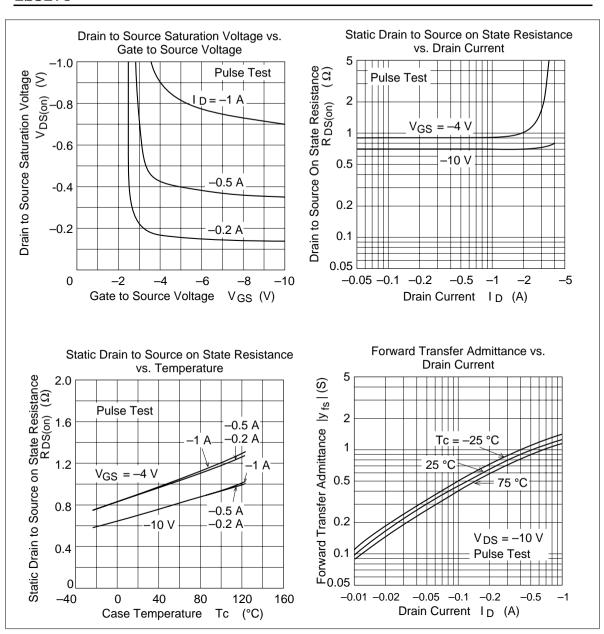
3. Marking is "MY".

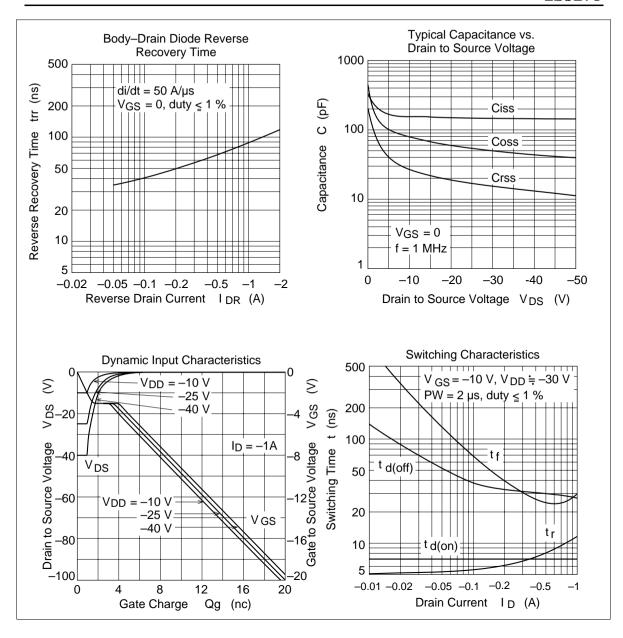
Electrical Characteristics ($Ta = 25^{\circ}C$)

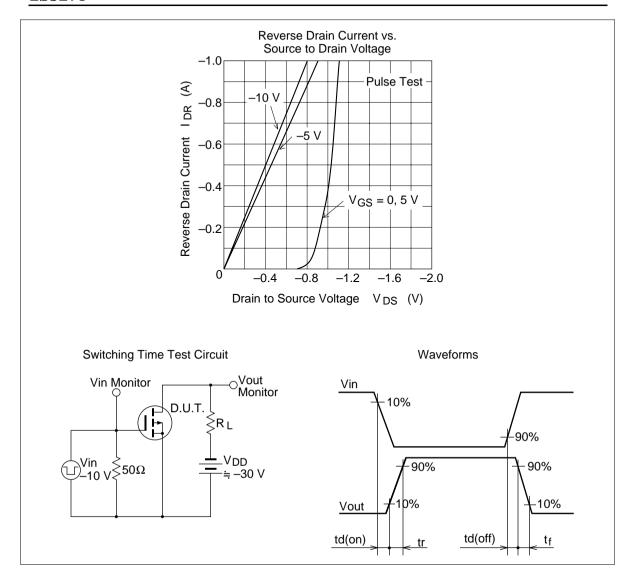
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	_	_	V	$I_{D} = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	$I_{\rm GSS}$	_	_	±5	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	-10	μΑ	$V_{DS} = -50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	_	-2.25	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state	R _{DS(on)}	_	0.7	0.83	Ω	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V}^{*1}$
resistance		_	0.9	1.2	Ω	$I_D = -0.5 \text{ A}, V_{GS} = -4 \text{ V}^{*1}$
Forward transfer admittance	y _{fs}	0.6	1.0	_	S	$I_D = -0.5 \text{ A}, V_{DS} = -10 \text{ V}^{*1}$
Input capacitance	Ciss	_	160	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	80	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	28	_	pF	
Turn-on delay time	t _{d(on)}	_	7	_	ns	$I_D = -0.5 \text{ A}, V_{GS} = -10 \text{ V},$
Rise time	t _r	_	8	_	ns	R _L = 60 Ω
Turn-off delay time	t _{d(off)}	_	30	_	ns	
Fall time	t _f	_	25	_	ns	
Body to drain diode forward voltage	V_{DF}	_	-1.1	_	V	$I_F = -1 A, V_{GS} = 0$
Body to drain diode reverse recovery time	t _{rr}	_	90	_	ns	$I_F = -1 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu\text{s}$

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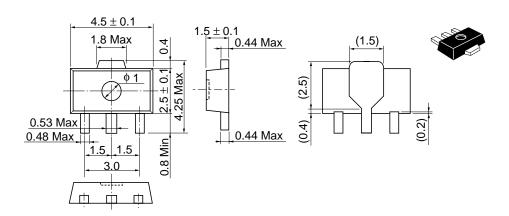








Unit: mm



Hitachi Code	UPAK
JEDEC	_
EIAJ	Conforms
Weight (reference value)	0.050 g

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